

P-Channel Enhancement Mode Power MOSFET

<p>Description</p> <p>The G75P04T uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.</p> <p>General Features</p> <ul style="list-style-type: none"> ● V_{DS} -40V ● I_D (at $V_{GS} = -10V$) -70A ● $R_{DS(ON)}$ (at $V_{GS} = -10V$) < 7mΩ ● $R_{DS(ON)}$ (at $V_{GS} = -4.5V$) < 9mΩ ● 100% Avalanche Tested ● RoHS Compliant <p>Application</p> <ul style="list-style-type: none"> ● Power switch ● DC/DC converters 	<p>Schematic diagram</p> <p>TO-220</p>
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Ordering Information			
Device	Package	Marking	Packaging
G75P04T	TO-220	G75P04	50pcs/Tube

Absolute Maximum Ratings $T_C = 25^\circ C$, unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-40	V
Continuous Drain Current	I_D	-70	A
Pulsed Drain Current (note1)	I_{DM}	-280	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	277	W
Single pulse avalanche energy (note2)	E_{AS}	306	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	°C

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	65	°C/W
Maximum Junction-to-Case	R_{thJC}	0.45	°C/W

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-40	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -40\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	-1	μA
Gate-Source Leakage	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1	-1.5	-2.5	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -20\text{A}$	--	5.2	7	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -20\text{A}$	--	6.8	9	
Forward Transconductance	g_{FS}	$V_{\text{DS}} = -5\text{V}, I_D = -20\text{A}$	--	42	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -20\text{V}, f = 1.0\text{MHz}$	--	6985	--	pF
Output Capacitance	C_{oss}		--	761	--	
Reverse Transfer Capacitance	C_{rss}		--	698	--	
Total Gate Charge	Q_g	$V_{\text{DD}} = -20\text{V}, I_D = -20\text{A}, V_{\text{GS}} = -10\text{V}$	--	106	--	nC
Gate-Source Charge	Q_{gs}		--	22	--	
Gate-Drain Charge	Q_{gd}		--	27	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -20\text{V}, I_D = -20\text{A}, R_G = 1\Omega$	--	15	--	ns
Turn-on Rise Time	t_r		--	12	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	70	--	
Turn-off Fall Time	t_f		--	18	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	-70	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = -20\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	-1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = -20\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = -100\text{A}/\mu\text{s}$	--	50	--	nC
Reverse Recovery Time	T_{rr}		--	53	--	ns

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition : $T_J=25^\circ\text{C}$, $V_{\text{DD}}=-40\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$
3. Identical low side and high side switch with identical R_g

Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

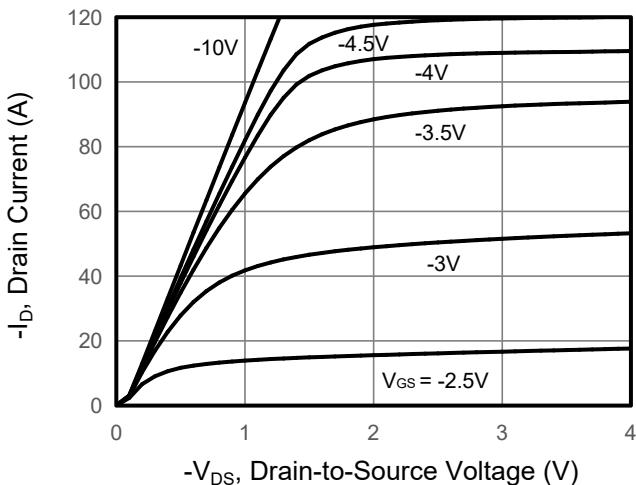


Figure 2. Transfer Characteristics

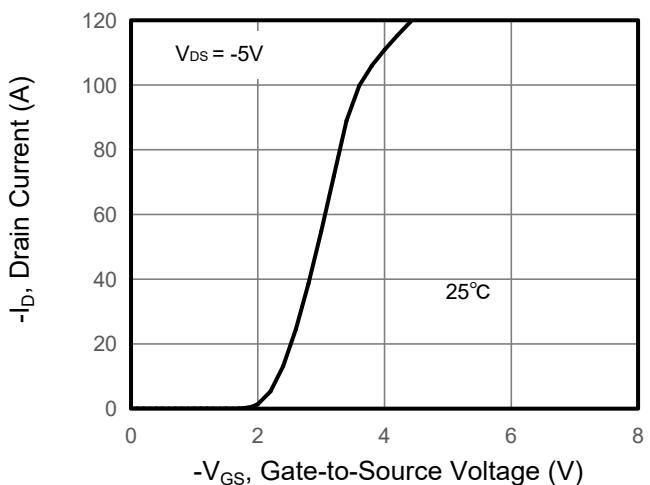


Figure 3. Drain Source On Resistance

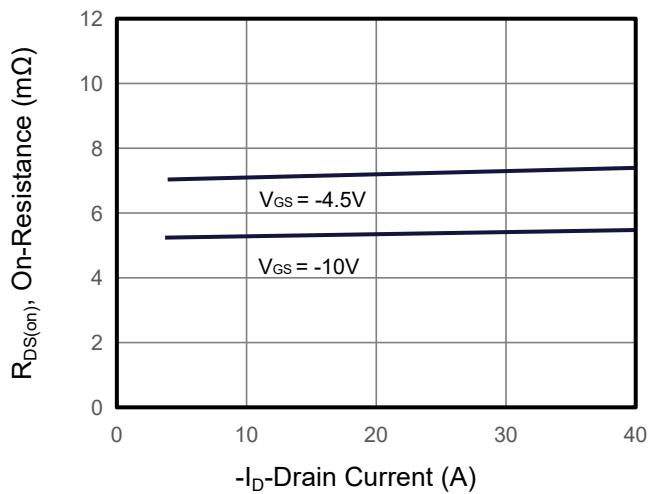


Figure 4. Gate Charge

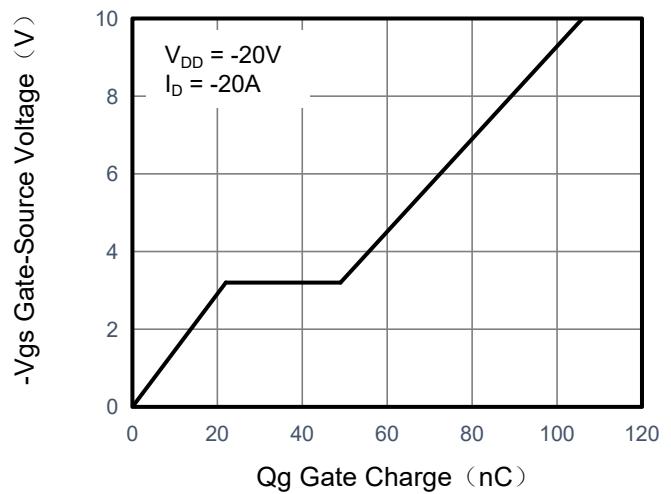


Figure 5. Capacitance

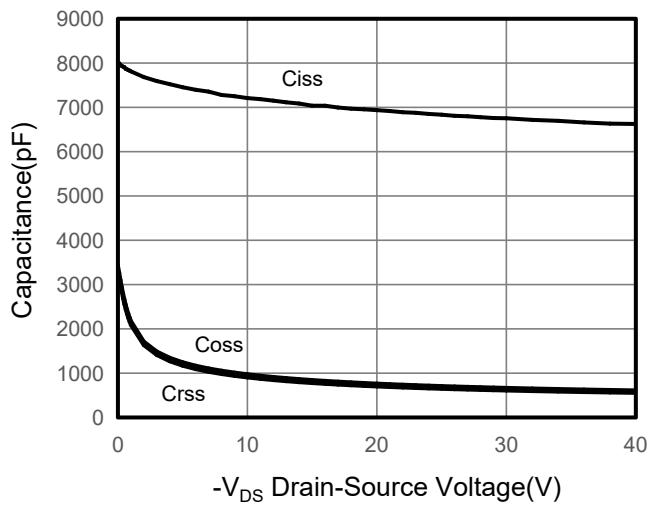
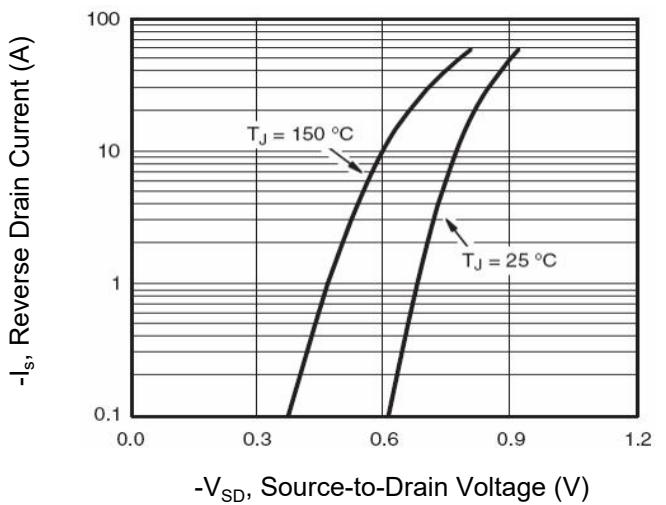


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

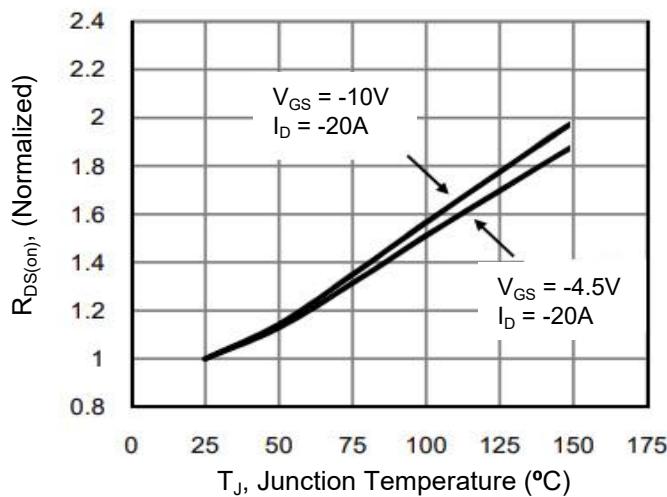


Figure 10. Safe Operation Area

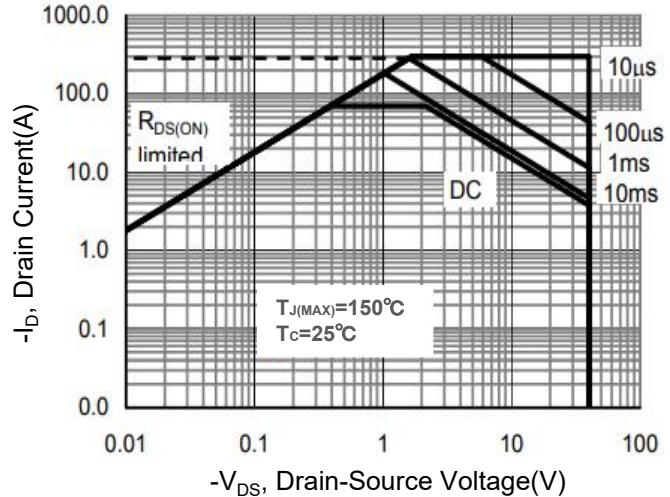
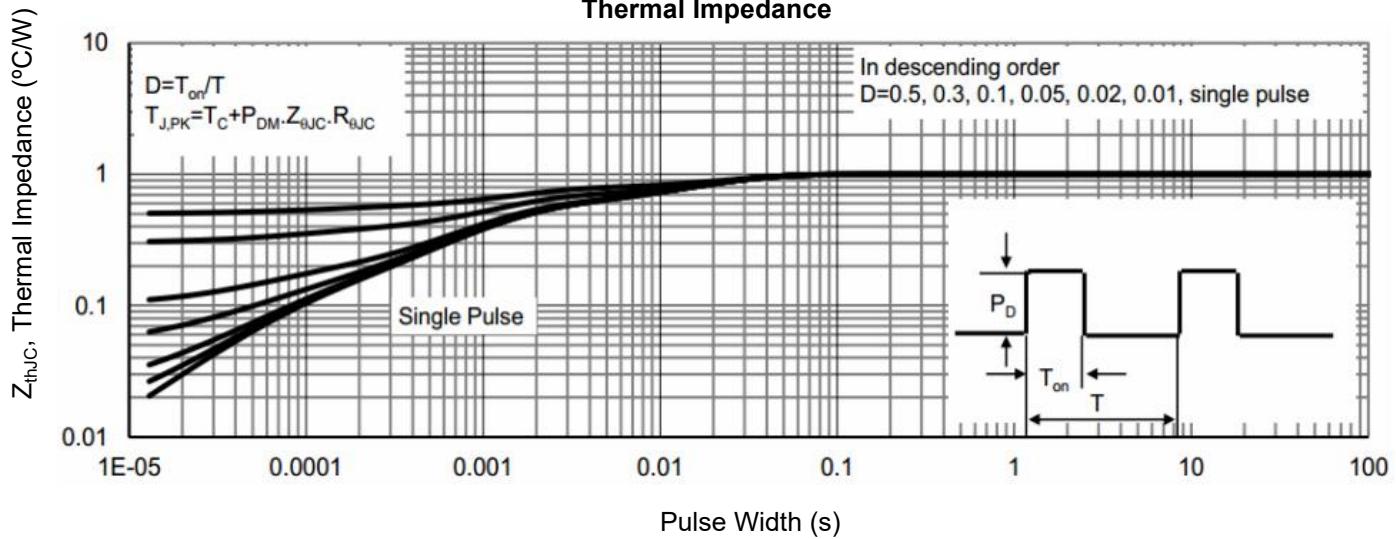
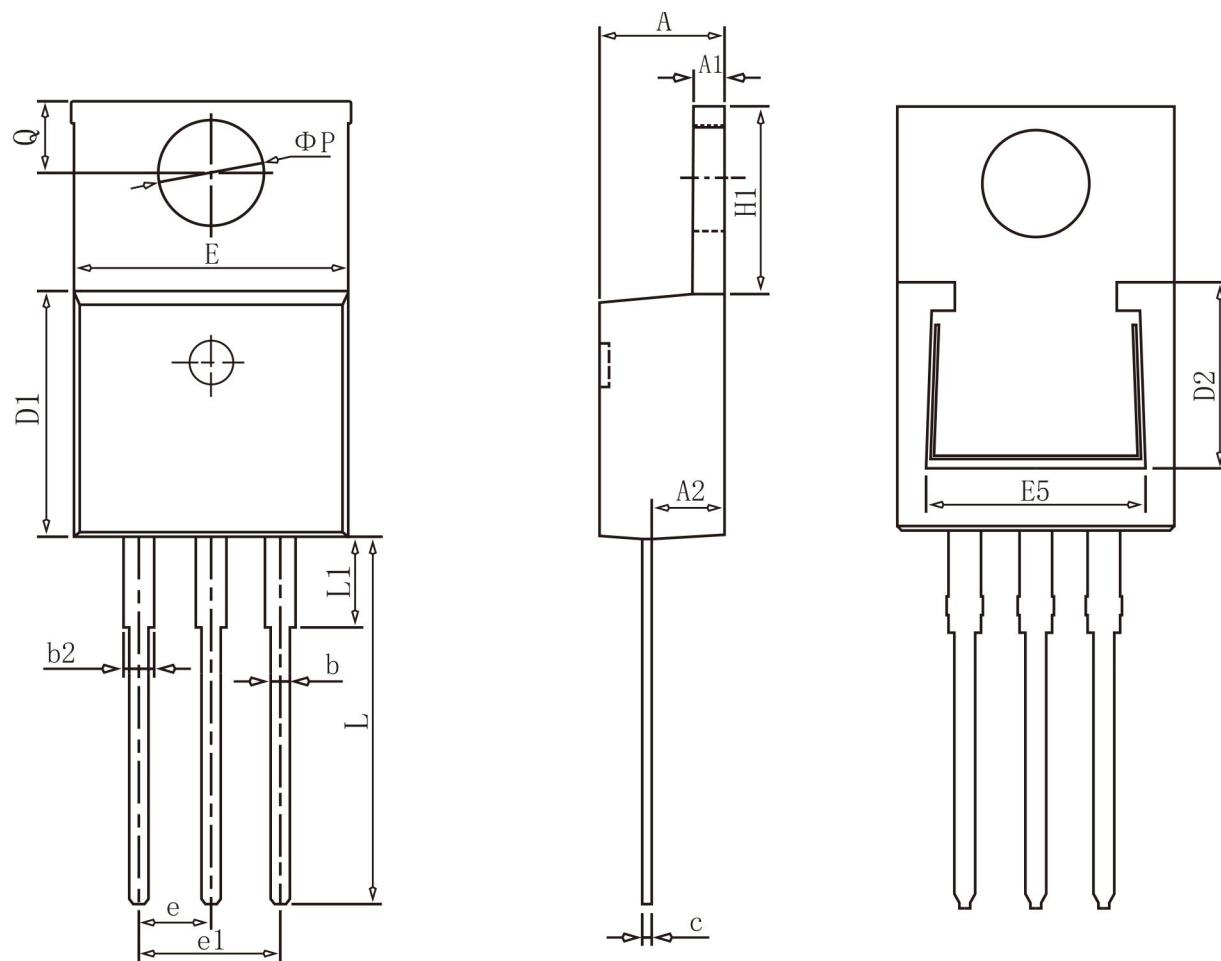


Figure 9. Normalized Maximum Transient Thermal Impedance



TO-220 Package Information



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
b	0.75	0.81	0.96
b2	1.22	1.27	1.47
c	0.30	0.38	0.48
D1	8.50	8.70	8.90
D2	5.20	—	—
E	9.86	10.16	10.36
E5	7.06	—	—
e	2.54BSC		
e1	5.08BSC		
H1	6.10	6.30	6.50
L	13.10	13.40	13.70
L1	—	3.75	4.10
ΦP	3.70	3.84	3.99
Q	2.54	2.74	2.94