

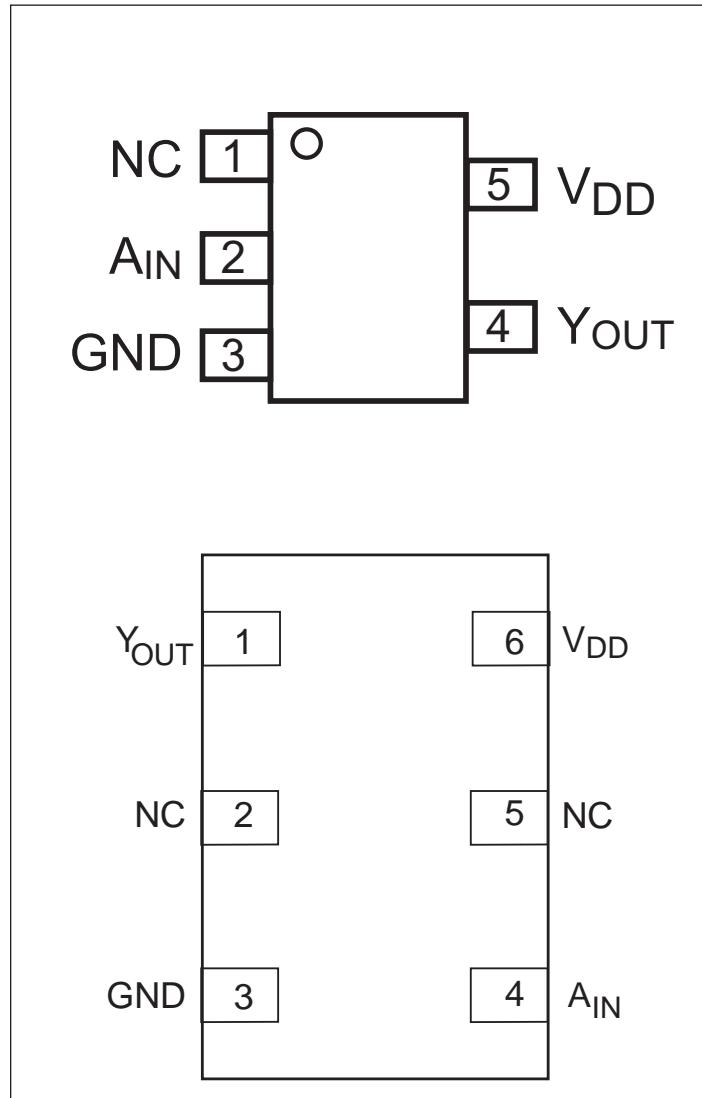
Features

- High-speed: tPD = 2.6ns typical into 50pF @ 5V VDD
- Unbuffered Output
- Broad operating range: VDD = 1.65V – 5.5V
- Power down high-impedance inputs/outputs
- Balanced output drive: $\pm 8\text{mA}$ at 3V VDD
- Packaging (Pb-free & Green available):
 - 5-pin SOT23(T)
 - 5-pin SC70(C)
 - 6-pin UDFN(ZR)

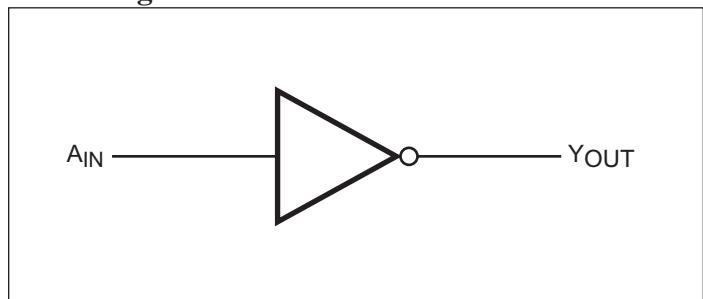
Description

The PI74STX1GU04 is an unbuffered inverter that operates over the 1.65V to 5.5V VDD operating range.

Pinout



Pin Configuration



Recommended Operating Conditions

Parameter	Condition	Min.	Max.	Units
Supply Voltage (VDD)		1.65	5.5	V
Input Voltage (VIN)		0	5.5	
Output Voltage (VOUT)		0	VDD	
Operating Temperature		-40	85	°C
Input Rise and Fall Time (tr, tf)	VDD = 1.8V, 2.5V±0.2V	0	20	ns/V
	VDD = 3.3V, ±0.3V	0	10	
	VDD = 5.0V, ±0.5V	0	5	

Notes:

1. Unused inputs must be held HIGH or LOW. They may not float.

Pin Description

Pin Names	Description
A _{IN}	Input
Y _{OUT}	Output

Function Table

Input	Output
A _{IN}	Y _{OUT}
L	H
H	L

Notes:

H = HIGH Logic Level

L = LOW Logic Level

Absolute Maximum Ratings

Supply Voltage (VDD)	-0.5V to +6V
DC Input Voltage (VIN).....	-0.5V to +6V
DC Output Voltage (VOUT).....	-0.5V to +6V
DC Input Diode Current (IIK)	-50mA to 20mA
DC Output Diode Current (IOK)	-50mA to 20mA
DC Output Current (IOUT)	±50mA
DC VCC/GND Current (ICC/IGND)	±50mA
Storage Temperature (TSTG).....	-65°C to +150°C
Junction Lead Temperature (IOS).....	200°C
Power Dissipation SOT23.....	200mW
SC70.....	150mW

Note:

Absolute maximum ratings are DC values beyond which the device may be damaged or have its useful life impaired. The datasheet specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Pericom does not recommend operation outside datasheet specifications.

DC Electrical Characteristics

Symbol	Parameter	VDD (V)	Description	TA = +25°C			TA = -40 to +85°C		Units
				Min.	Typ	Max.	Min.	Max.	
V _{IH}	HIGH Level Input Voltage	1.8-2.7 3.0-5.5		0.85 V _{DD} 0.8 V _{DD}			0.85 V _{DD} 0.8 V _{DD}		V
V _{IL}	LOW Level Input Voltage	1.8-2.7 3.0-5.5				0.15 V _{DD} 0.2 V _{DD}		0.15 V _{DD} 0.2 V _{DD}	
V _{OH}	HIGH Level Output Voltage	1.65 1.8 2.3 3.0 4.5	V _{IN} = V _{IL}	I _{OH} = -100µA	1.55 1.6 2.1 2.7 4.0	1.65 1.8 2.3 3.0 4.4		1.55 1.6 2.1 2.7 4.0	
		1.65 2.3 3.0 3.0 4.5		I _{OH} = -4mA I _{OH} = -4mA I _{OH} = -8mA I _{OH} = -12mA I _{OH} = -16mA	1.29 1.9 2.4 2.3 3.8	1.42 2.14 2.75 2.61 4.07		1.29 1.9 2.4 2.3 3.8	
		1.65 1.8 2.3 3.0 4.5		I _{OH} = 100µA		0.00 0.00 0.00 0.00 0.00	0.1 0.1 0.1 0.1 0.1	0.1 0.1 0.1 0.1 0.1	
		1.65 2.3 3.0 3.0 4.5		I _{OH} = 4mA I _{OH} = 4mA I _{OH} = 8mA I _{OH} = 12mA I _{OH} = 16mA		0.08 0.10 0.48 0.28 0.31	0.24 0.3 0.4 0.55 0.55	0.24 0.3 0.4 0.55 0.55	
		0 to 5.5	V _{IN} = 5.5V, GND				±1	±10	µA
I _{OFF}	Power Off Leakage Current	0.0	V _{IN} or V _{OUT} = 5.5V	-1		1	-1	1	
ICC	Quiescent Supply Current	1.65 - 5.5	V _{IN} = 5.5V, GND			2.0		20	

AC Electrical Characteristics

Symbol	Parameter	VCC (V)	Conditions	TA = +25°C			TA = -25°C to +85°C		Units	Fig. No.
				Min.	Typ.	Max.	Min.	Max.		
t_{PLH} t_{PHL}	Propagation Delay	1.8 ± 0.15	$C_L = 15\text{pF}$, $R_L = 1\text{M}\cdot\text{ohm}$	0.7	5.0	6.3	0.7	6.9	ns	1 3
		2.5 ± 0.2		0.5	3.0	3.7	0.5	4.1		
		3.3 ± 0.3		0.5	2.3	3.0	0.5	3.3		
		5.0 ± 0.5		0.5	1.8	2.4	0.5	2.6		
t_{PLH} t_{PHL}	Propagation Delay	3.3 ± 0.3	$C_L = 50\text{pF}$, $R_L = 500\text{-ohm}$	0.5	3.2	4.5	0.5	5.0	pF	1 3
		5.0 ± 0.5		0.5	2.6	3.4	0.5	3.7		
C_{IN}	Input Capacitance	0			4					
C_{PD}	Power Dissipation Capacitance ¹	3.3 5.0			6.3 9.5					2

Notes:

1. CPD is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (ICCD) at no output loading and operating at 50% duty cycle (see Figure 2). CPD is related to ICCD dynamic operating current by the expression: $ICCD = (CPD)(VCC)(fIN) + (ICC \text{ static})$.

AC Loading and Waveforms

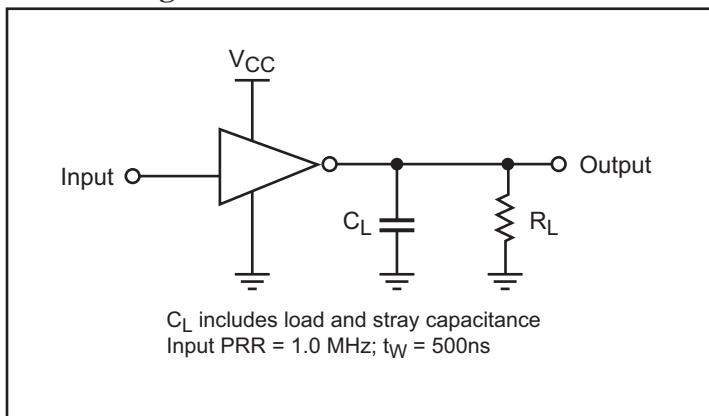


Figure 1. AC Loading and Waveforms

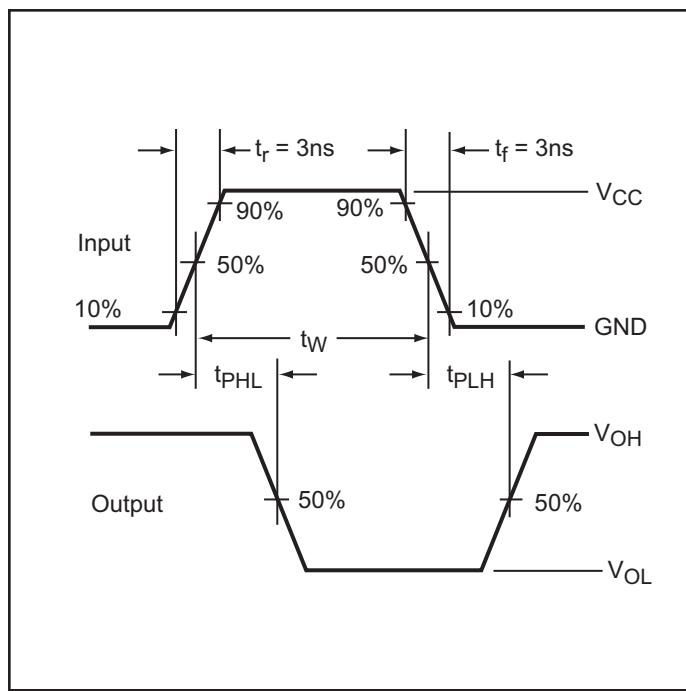
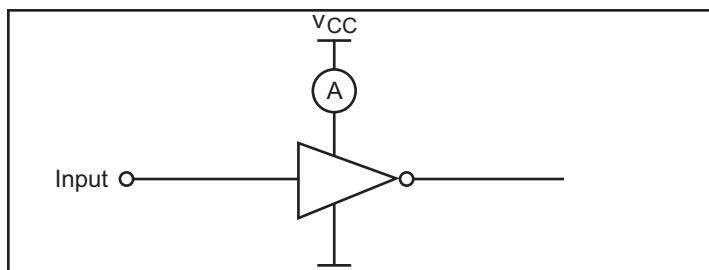


Figure 3. AC Waveform

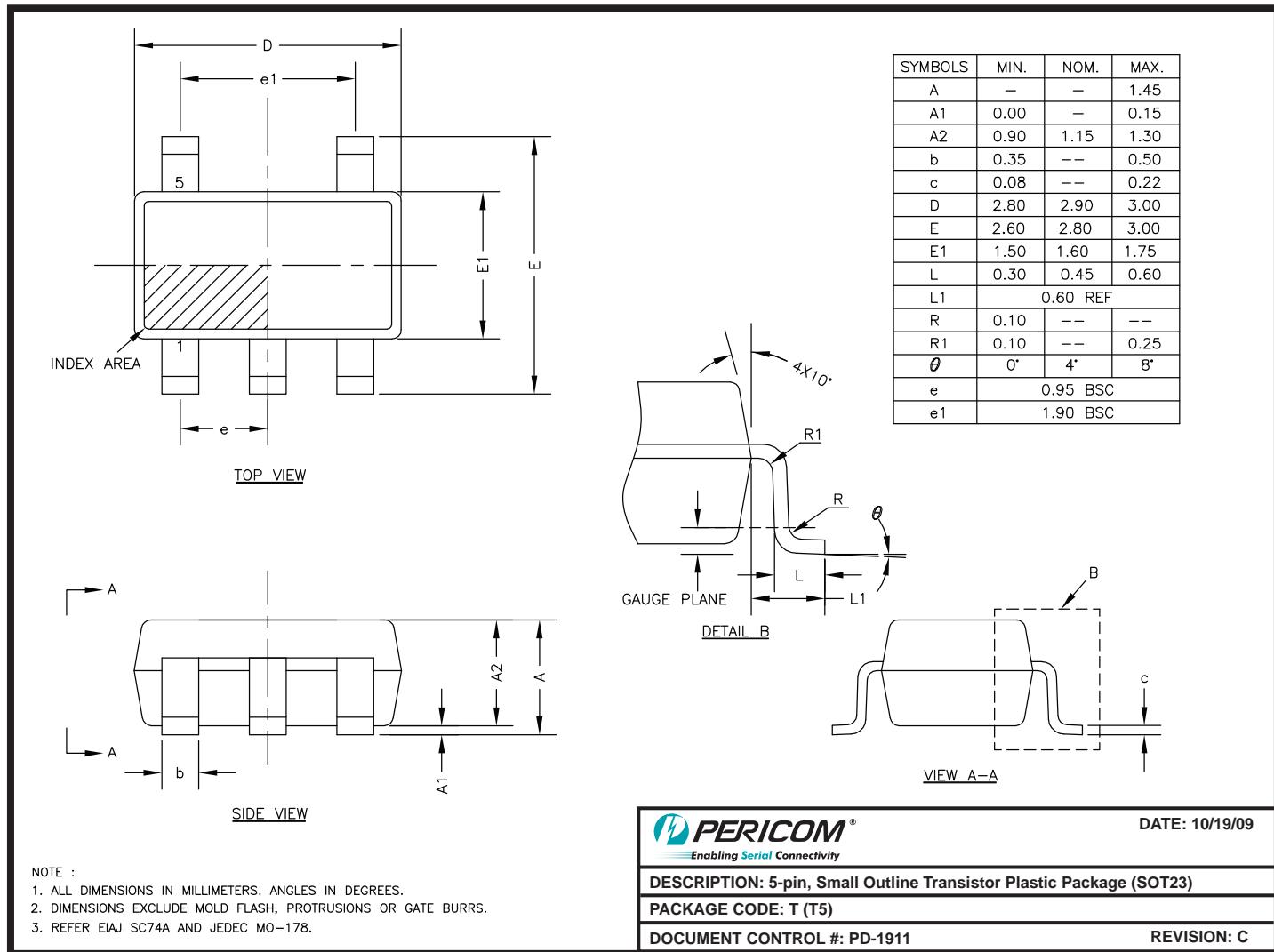


Application Note: When operating the PI74STX1GU04A's unbuffered output stage in its linear region, as in oscillator applications, the maximum power ratings of the device and package need to be carefully observed. The high drive design of the output stage can cause substantial simultaneous condition currents when the stage is in its linear region.

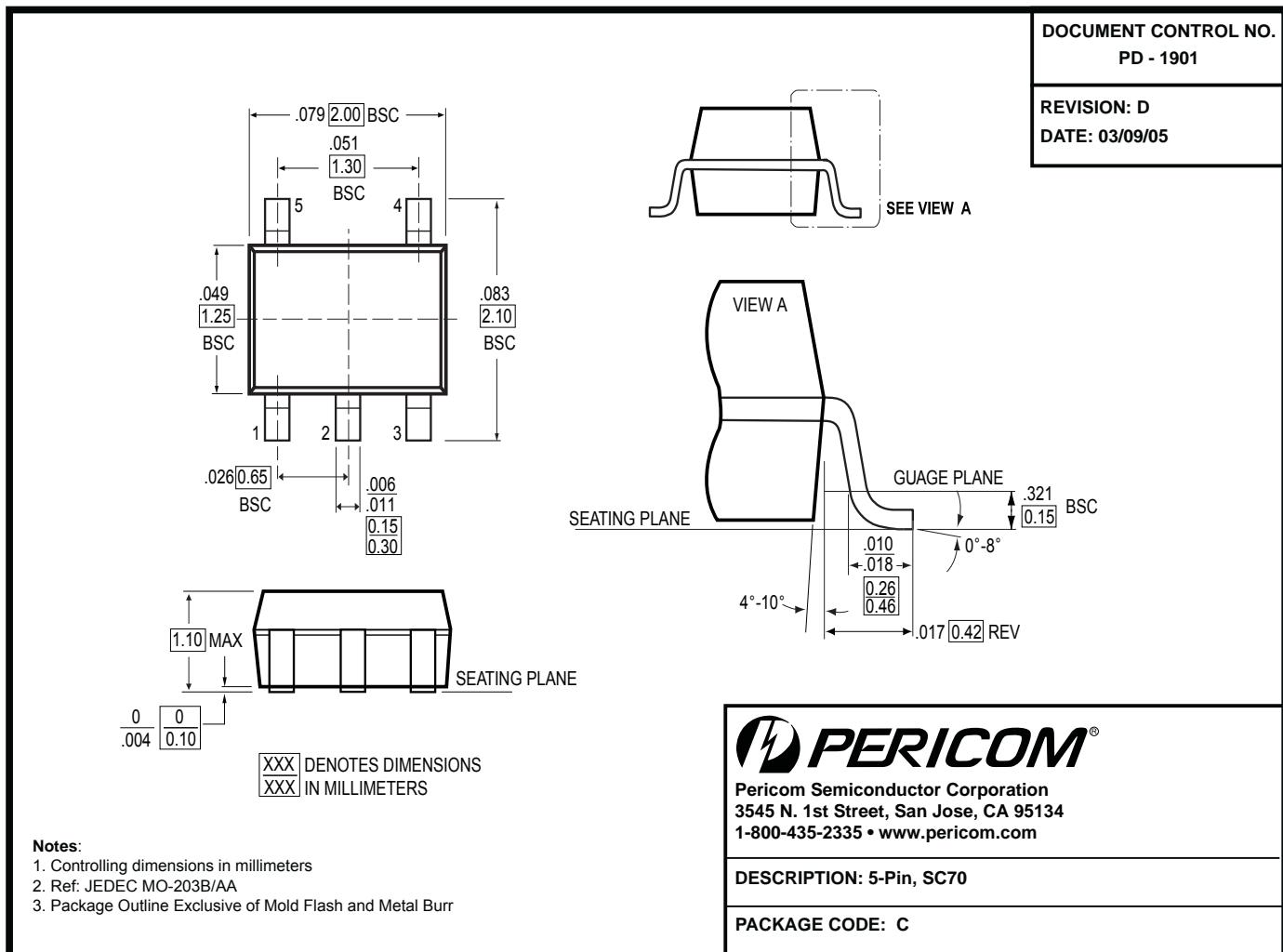
See I_{CCPEAK} Specification on page 2.

Input = AC Waveform; $t_r = t_f = 1.8\text{ns}$; PRR = 10MHz; Duty Cycle = 50%

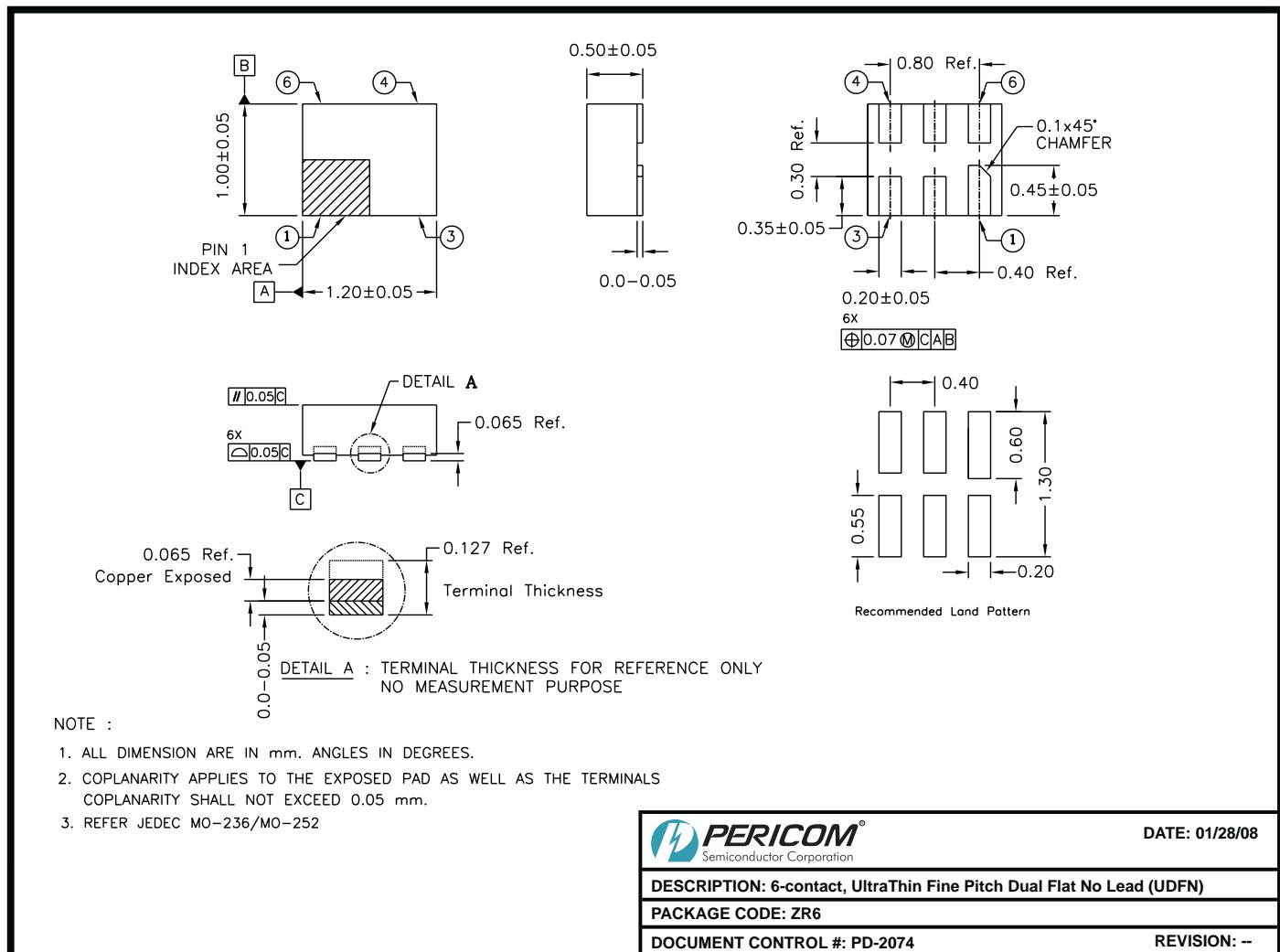
Figure 2. ICCD Test Circuit

Packaging Mechanical: 5-Pin SOT23 (T)


09-0130


Notes:

1. Controlling dimensions in millimeters
2. Ref: JEDEC MO-203B/AA
3. Package Outline Exclusive of Mold Flash and Metal Burr



DATE: 01/28/08

DESCRIPTION: 6-contact, UltraThin Fine Pitch Dual Flat No Lead (UDFN)

PACKAGE CODE: ZR6

DOCUMENT CONTROL #: PD-2074

REVISION: --

08-0035

Note:

- For latest package info, please check: <http://www.pericom.com/products/packaging/mechanicals.php>

Ordering Information⁽¹⁻³⁾

Ordering Code	Package Code	Package Description	Top Marking
PI74STX1GU04TEX	T	5-pin SOT23, Pb-free & Green	BH
PI74STX1GU04CEX	C	5-pin SC70, Pb-free & Green	BH
PI74STX1GU04ZREX	ZR	6-pin UDFN, Pb-Free and Green	BH

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X suffix = Tape/Reel