



## P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
- 30	0.0075 at V <sub>GS</sub> = - 10 V	- 15			
- 30	0.011 at V <sub>GS</sub> = - 4.5 V	- 12.3			

#### **FEATURES**

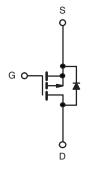
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET® Power MOSFET

# **HALOGEN**

**FREE** 

#### **APPLICATIONS**

- Notebook
  - Load Switch
  - Battery Switch



P-Channel MOSFET

	SO-8		
S 1 S 2 S 3 G 4		8 7 6 5	D D D
	Top View		

Ordering Information: Si4413ADY-T1-E3 (Lead (Pb)-free) Si4413ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		$V_{DS}$	- 30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current /T 150 °C\d	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 15	- 10.5	٨
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 11.8	- 8.3	
Pulsed Drain Current		I <sub>DM</sub>	- 50		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 2.7	- 1.36	
Mariana Barra Birata Arang	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	3.0	1.5	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.9	0.95	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manifestory Investigation to Application	t ≤ 10 s	R <sub>thJA</sub>	33	42		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	70	84	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	16	21		

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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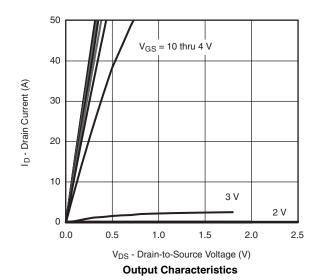
<b>SPECIFICATIONS</b> $T_J = 25$ °C, unless otherwise noted							
Parameter	Symbol	Test Conditions		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$ - 1.0			- 3.0	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C		- 10	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 30			Α	
Davis Course Co Clata Davistana 3	R <sub>DS(on)</sub>	$V_{GS} = -10 \text{ V}, I_D = -13 \text{ A}$	0.0063 0.007		0.0075	Ω	
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 10 A		0.0083	0.011		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, I_{D} = -13 \text{ A}$		50		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = -2.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.74	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			61	95		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -13 \text{ A}$		15.5		nC	
Gate-Drain Charge	$Q_{gd}$			32			
Turn-On Delay Time	t <sub>d(on)</sub>			21	35		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 15 $\Omega$		18	30	no	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1 A, $V_{GEN}$ = - 10 V, $R_G$ = 6 $\Omega$		170	260	ns	
Fall Time	t <sub>f</sub>			97	150		
Gate Resistance	R <sub>g</sub>			3.4		Ω	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = -2.1 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		70	110	ns	

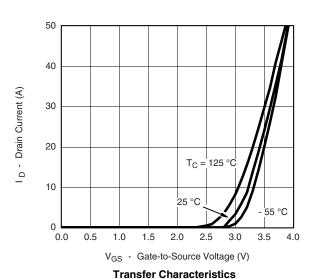
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

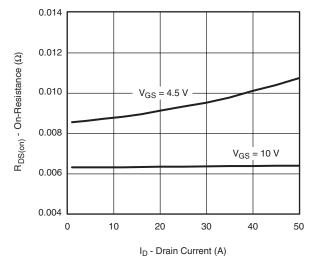




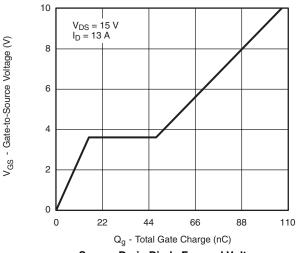




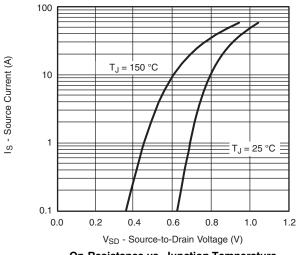
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



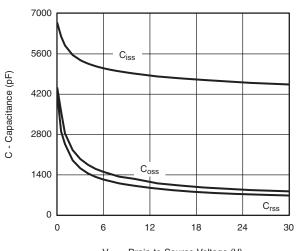
#### On-Resistance vs. Drain Current



Source-Drain Diode Forward Voltage

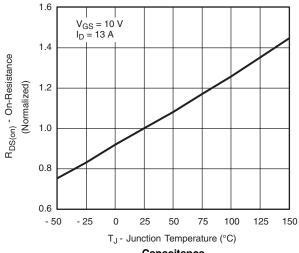


On-Resistance vs. Junction Temperature

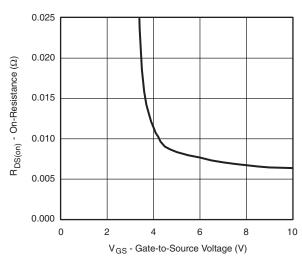


V<sub>DS</sub> - Drain-to-Source Voltage (V)





Capacitance

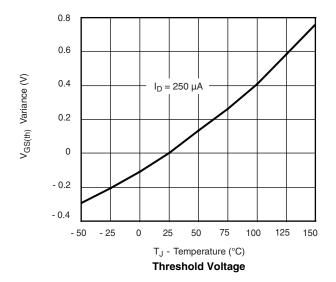


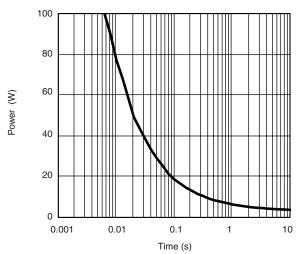
On-Resistance vs. Gate-to-Source Voltage

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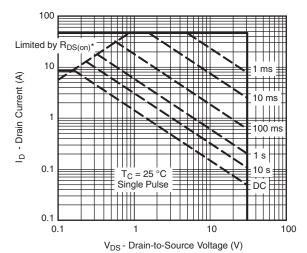
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



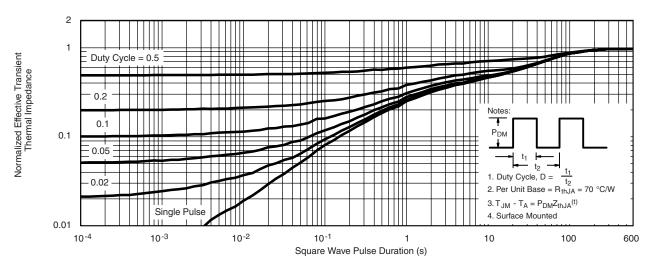


Single Pulse Power, Junction-to-Ambient



 $^{\star}$   $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

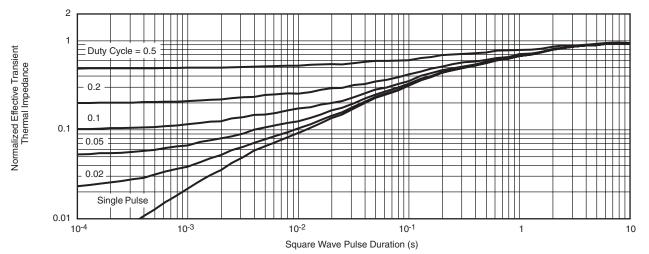
#### Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

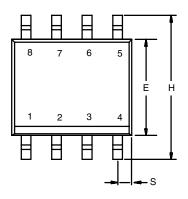


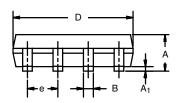
Normalized Thermal Transient Impedance, Junction-to-Foot

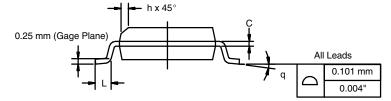
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







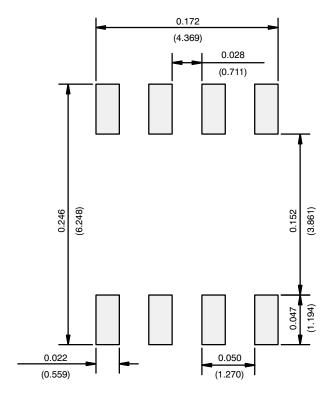
	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27 BSC		0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

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#### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

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